

~~PATENT NUMBER~~

## U.S. UTILITY Patent Application

JC 2  
JC 858

<p>O.I.P.F.</p> <p>SCANNED <u>4/10/2017</u> Q.A. <u>4/11/2017</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/729080	CONT/PRIOR	CLASS <del>708</del> 706	SUBCLASS 1	ART UNIT <del>2121</del>	EXAMINER <i>Thompson</i>
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**TS** Kazutaka Saitoh

## APPLICANTS

Smart card and circuitry layout thereof

**TITLE**

PTO-2040  
12/89

## ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
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